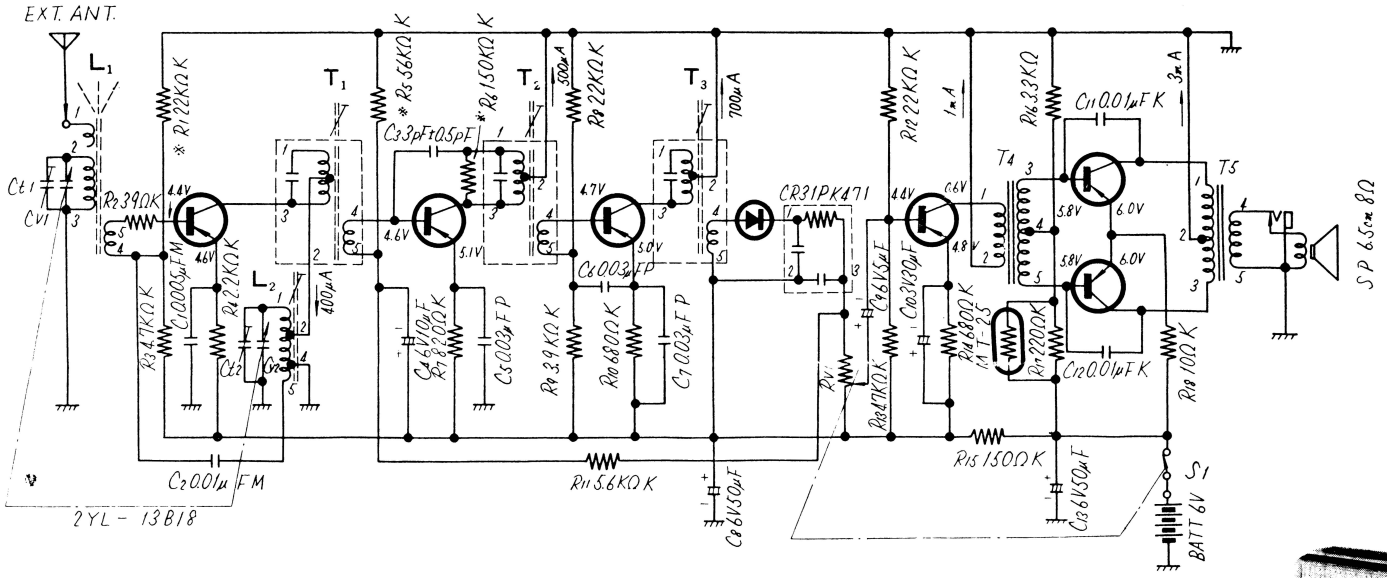
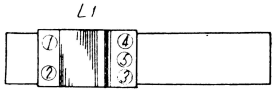
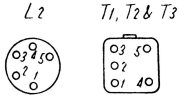
SCHEMATIC DIAGRAM

TR_1 Conv.	TR_2 Ist. IF Amp.	TR_3 2nd. IF Amp.	D_1 Det & A.G.C	TR_4 A.F. Amp.	TR_5 & TR_6 Out put
2SA102	2SA101	2SA101	0A70	2SB175	2SB176×2

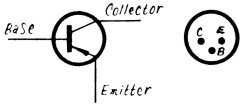


Sensitivity : (for 5mW)	100μV/m	
Power Output :	150mW	undistorted
	250mW	maximum

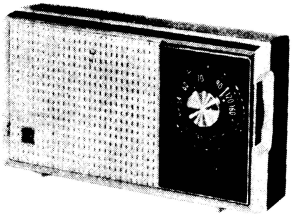




BOTTOM VIEW



Transistor Bottom View



Notes:

1. S_1 : Power switch now at "OFF" position.
2. Voltage read under no signal conditions with a 10KΩ/volt voltmeter and are negative with respect to ground.
3. The resistor dotted in the diagram are the standard values which may be variable according to the features of resistors.

R_1	18KΩ,	27KΩ
R_5	47KΩ,	68KΩ
R_6	56KΩ,	100KΩ

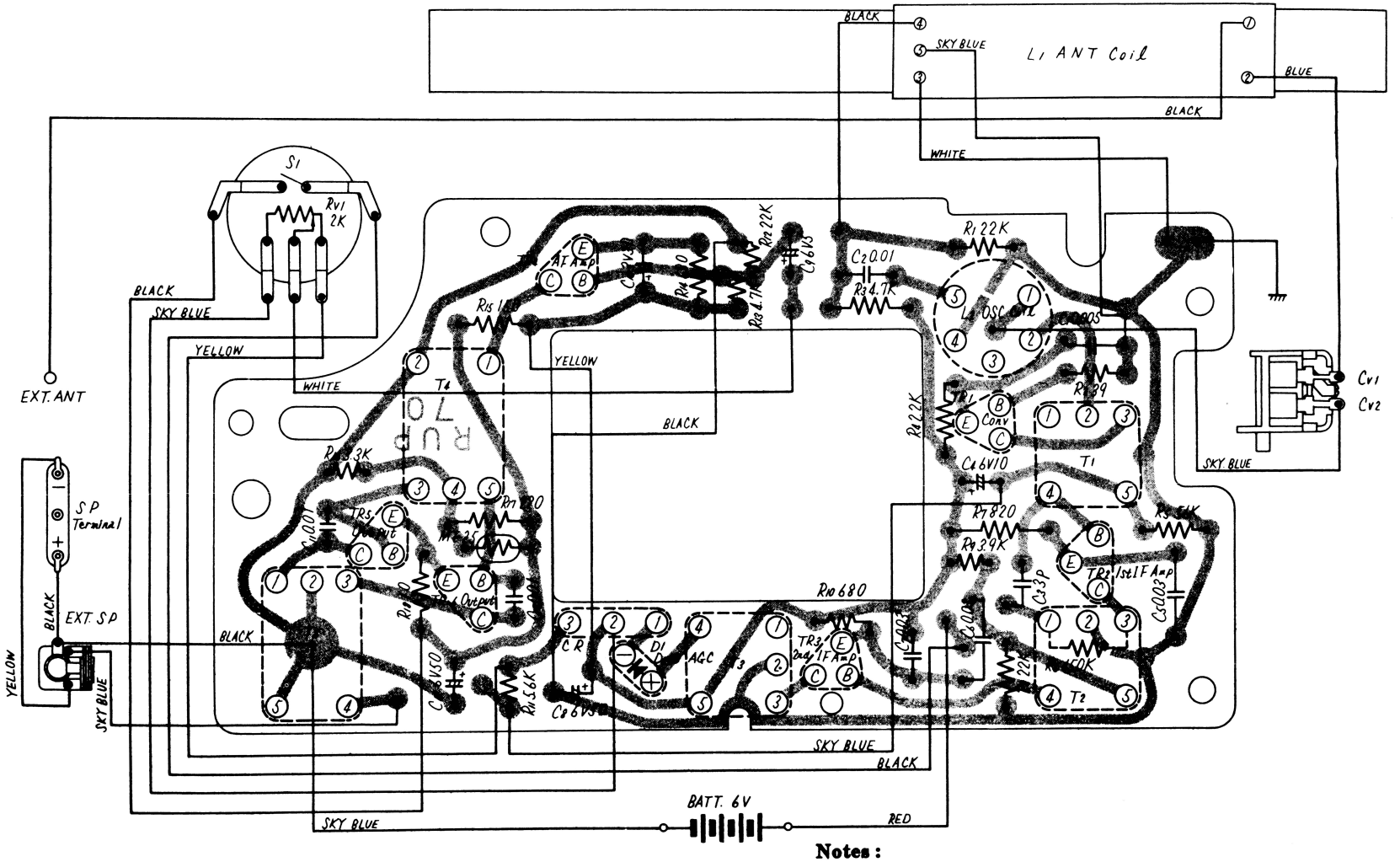
4. The letter signs (J.K.M.P.) in the circuit diagrom show allowable deviation of resistors and capacitors as follows.

   J: ±5% K: ±10% M: ±20% P: $^{+100\%}_{-\ 0\%}$

5. Battery Current :

No Signal	9mA
Maximum Signal	85mA

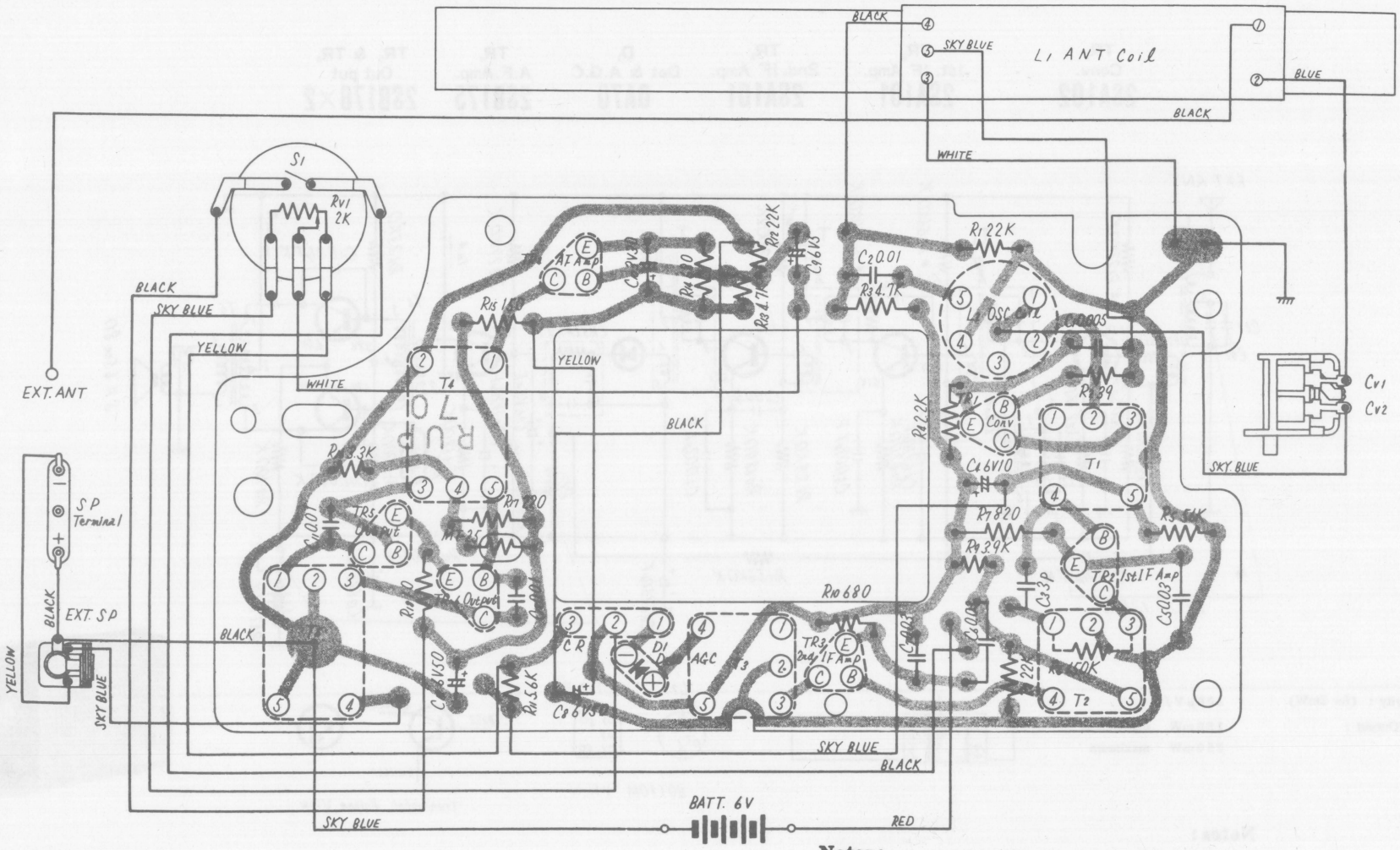
— PRINTED CIRCUIT BOARD —



Notes :

1. All resistor values in ohms (K=1000)
2. All capacitor values in microfarads (P=$\mu\mu$F).
3. S_1 : Power switch now at "OFF" position.
4. Other parts in the printed circuit board are indicated in dotted lines.
5. T_1.....1st IF Transformer
   T_2.....2nd IF Transformer
   T_3.....3rd IF Transformer
   T_4.....Input Transformer
   T_5.....Output Transformer

— PRINTED CIRCUIT BOARD —



Notes:

1. All resistor values in ohms (K=1000)
2. All capacitor values in microfarads (P=$\mu\mu$F).
3. S_1: Power switch now at "OFF" position.
4. Other parts in the printed circuit board are indicated in dotted lines.
5. T_1.....1st IF Transformer
   T_2.....2nd IF Transformer
   T_3.....3rd IF Transformer
   T_4.....Input Transformer
   T_5.....Output Transformer